

What is claimed is:

1. A traffic shaper for transferring variable-length packets received from a plurality of input ports to one output port while guaranteeing 5 preliminarily designated minimum bandwidths, comprising:

a plurality of buffer memories for temporarily storing the variable-length packets received from said input ports;

10 a bandwidth controller for specifying a buffer memory from which a packet is to be read out; and a read controller for reading out a variable-length packet from a buffer memory specified by said bandwidth controller and transmitting the 15 packet to said output port,

said bandwidth controller comprising:

a plurality of leaky bucket units prepared in correspondence with said buffer memories;

20 outputting a numerical value indicative of a difference between a level count value output from each of said leaky bucket units and a threshold corresponding to the preliminarily designated minimum guaranteed bandwidth; and

25 an output queue designation unit for specifying

a buffer memory from which a packet is to be read out on the basis of the difference values output from said level deviation detectors, and

each of said leaky bucket units comprising:

5 a level counter for decrementing the count value at a predetermined rate; and

level raising means for increasing the count value of said level counter, in response to reading out of a packet from a corresponding buffer memory by said 10 read controller, by a value proportional to the length of said packet.

2. The traffic shaper according to claim 1, wherein when the level count value output from each of said

15 leaky bucket units becomes equal to or smaller than said threshold, each of said level deviation detectors outputs zero as said difference value, and

said output queue designation unit has a register for indicating a packet storage status in each of said 20 buffer memories, validates output values from the level deviation detectors corresponding to the buffer memories, each of which is storing at least one packet to be read out, indicated by said register among the validated difference values, and selects one minimum 25 value.

3. The traffic shaper according to claim 2, wherein
said output queue designation unit includes means for
selecting one minimum value in a round-robin manner
5 when a plurality of difference values having the same
minimum values exist in said validated difference
values.

4. The traffic shaper according to claim 1, wherein
each of said leaky bucket units includes means for
supplying a variable unitary increment value to said
level raising means, said variable unitary increment
value being changed step by step according to the
current level count value of said level counter, and
15 said level raising means increases the count value
of said level counter in accordance with the product
of said variable unitary increment value and said packet
length.

20 5. The traffic shaper according to claim 1, wherein
each of said leaky bucket units includes an increment
value table for indicating unitary increment values
corresponding to a plurality of level zones defined
within the range of the level count values of said level
25 counter, and

said level raising means increases the count value of said level counter in accordance with the product of said packet length and a unitary increment value read out from said increment value table according to 5 the current level count value of said level counter.

6. The traffic shaper according to claim 5, wherein each of said plurality of leaky bucket units selectively uses one of increment value tables having different 10 definitions of level zones in accordance with the preliminarily designated minimum guaranteed bandwidth value.

7. The traffic shaper according to claim 4, wherein 15 the unitary increment value supplied to said level raising means increases step by step as the level count value of said level counter decreases.

8. A traffic shaper for temporarily storing 20 variable-length packets received from a plurality of input ports in buffer memories corresponding to the input ports, reading out the packets stored in the buffer memories, and transferring the packets to one output port while guaranteeing minimum bandwidths 25 within maximum allowable bandwidths preliminarily

designated for each of the buffer memories, comprising:

a bandwidth controller for specifying a buffer memory from which a packet is to be read out, having

a plurality of leaky bucket units prepared in

5 correspondence with said buffer memories, and

an output queue designation unit for specifying a buffer memory from which a packet is to be read out,

among buffer memories of which level count value is equal to or smaller than a first threshold corresponding

10 to the preliminarily designated maximum allowable bandwidth, on the basis of difference values each indicating the difference between a level count value

output from each of said leaky bucket units and a second threshold corresponding to the preliminarily designated minimum guaranteed bandwidth, and

15 each of said leaky bucket units having:

a level counter for decrementing the count value at a predetermined rate; and

level raising means for increasing the count value 20 of said level counter, in response to reading out of a packet from a corresponding buffer memory, by a value proportional to the length of said packet.

9. The traffic shaper according to claim 8, wherein

25 said output queue designation unit comprises:

a plurality of level deviation detectors each for outputting a difference value between the level count value output from one of said leaky bucket units and said second threshold; and

5 a buffer selector for specifying a buffer memory from which a packet is to be read out on the basis of the difference values output from said level deviation detectors,

each of said level deviation detectors outputs zero as said difference value when the level count value output from the leaky bucket units becomes equal to or smaller than said second threshold, and

10 said buffer selector validates difference values output from level deviation detectors corresponding 15 to buffer memories each of which is storing at least one packet to be read out, and selects one minimum value among the validated difference values.

10. The traffic shaper according to claim 9, wherein
20 said buffer selector selects one minimum value in a round-robin manner when a plurality of same minimum values exist in said validated difference values.

11. The traffic shaper according to claim 8, wherein
25 each of said leaky bucket units includes means for

supplying a variable unitary increment value, which increases step by step as the level count value of said level counter decreases, to said level raising means, and

5 said level raising means increases the count value of said level counter in accordance with the product of said variable unitary increment value and said packet length.

10 12. The traffic shaper according to claim 8, wherein each of said plurality of leaky bucket units includes an increment value table for indicating different unitary increment values corresponding to a plurality of level zones defined within the range of the level 15 count values of said level counter, and

 said level raising means increases the count value of said level counter in accordance with the product of said packet length and a unitary increment value corresponding to the current level count value of said 20 level counter in said increment value table.

13. The traffic shaper according to claim 12, wherein each of said plurality of leaky bucket units selectively uses one of increment value tables having different 25 definitions of level zones in accordance with the

preliminarily designated minimum guaranteed bandwidths.

14. A bandwidth controller for reading out
5 variable-length packets from a plurality of buffer
memories while guaranteeing a minimum bandwidth
preliminarily designated for each buffer memory,
comprising:

a plurality of leaky bucket units prepared in
10 correspondence with said buffer memories;

an output queue designation unit for specifying
a buffer memory from which a packet is to be read out,
on the basis of difference values each indicating the
difference between a level count value output from one
15 of said leaky bucket units and a threshold corresponding
to a minimum guaranteed bandwidth preliminarily
designated for each buffer memory; and

a read controller for reading out one
variable-length packet from a buffer memory specified
20 by said output queue designation unit,

each of said leaky bucket units having:

a level counter for decrementing the count value
at a predetermined rate; and

level raising means for increasing the count value
25 of said level counter, in response to reading out of

a packet from a corresponding buffer memory, by a value proportional to the length of said packet, and
said level raising means for increasing the count value of said level counter in accordance with the 5 product of a variable unitary increment value and a packet length, said variable unitary increment value increasing step by step as the level count value of said level counter decreases.

10 15. The bandwidth controller according to claim 14, wherein each of said leaky bucket units has an increment value table for indicating unitary increment values corresponding to a plurality of level zones defined within the range of the level count values of said level counter, the threshold corresponding to the minimum 15 guaranteed bandwidth designated for the buffer memory being positioned almost in the center of one of said level zones, and wherein

20 said level raising means increases the count value of said level counter in accordance with the product of said packet length and a unitary increment value corresponding to the current level count value of said level counter in said table.

25 16. A traffic shaper for transferring

variable-length packets received from a plurality of input ports to one output port while guaranteeing preliminarily designated minimum bandwidths, comprising:

5 a plurality of buffer memories for temporarily storing the variable-length packets received from said input ports;

 a bandwidth controller for specifying a buffer memory from which a packet is to be read out; and

10 a read controller for reading out one variable-length packet from the buffer memory specified by said bandwidth controller and transmitting the packet to said output port,

 wherein said bandwidth controller includes means for assigning an unoccupied bandwidth in said output port to each of said buffer memories in accordance with a rate of the minimum bandwidth designated to each of said buffer memories, and

 said read control means reads out packets from each of said buffer memories in a transmission rate equal to or higher than the minimum guaranteed bandwidth in response to the assigning of said unoccupied bandwidth from said bandwidth controller.